

A.D.J. DHARMAMBAL POLYTECHNIC COLLEGE, NAGAPATTINAM**(A Govt. Aided Co-Educational Institution)****CONTINUOUS ASSESSMENT II SEPTEMBER 24****OVERALL TIME TABLE****INTERNAL EXAM CELL - CIRCULAR Dated: 03.09.24****I YEAR – I SEMESTER****Time: 2.30 pm to 4.30 pm**

Date/Dept.	03.09.24	04.09.24	06.09.24
I YEAR-A BATCH	Tamil Marabu	Basic Mathematics	Communicative English
I YEAR-B BATCH	Communicative English	Tamil Marabu	Basic Mathematics

II YEAR-III SEMESTER**Time: 2.30 pm to 4.30 pm**

Date/Dept.	05.09.24	06.09.24
ECE	Electronic Devices And Circuits	Digital Electronics
CE	RDBMS	Digital Logic Design
ICE	Measurements And Instruments	Sensors And Transducers
EEE	Electrical Machines I	Electrical Circuit Theory
MECH	Strength Materials	Manufacturing Process

III YEAR-V SEMESTER**Time: 2.30 pm to 4.30 pm**

Date/Dept.	03.09.24	04.09.24	05.09.24	06.09.24
ECE	Analog and Digital Communication Systems	Microcontroller and its Applications	---	Very Large Scale Integration
CE	---	Python Programming	Cloud Computing & IOT	Component Based Technology
ICE	---	Microcontroller	Control Engineering	Process Control Instrumentation
EEE	---	Generation Transmission and Switchgear	Micro Controller and its Applications	Control of Electrical Machines

COE**PRINCIPAL**

Signature of HOD's

1. ECE	2. CE	3. ICE	4. EEE	5. MECH	6. I YEAR